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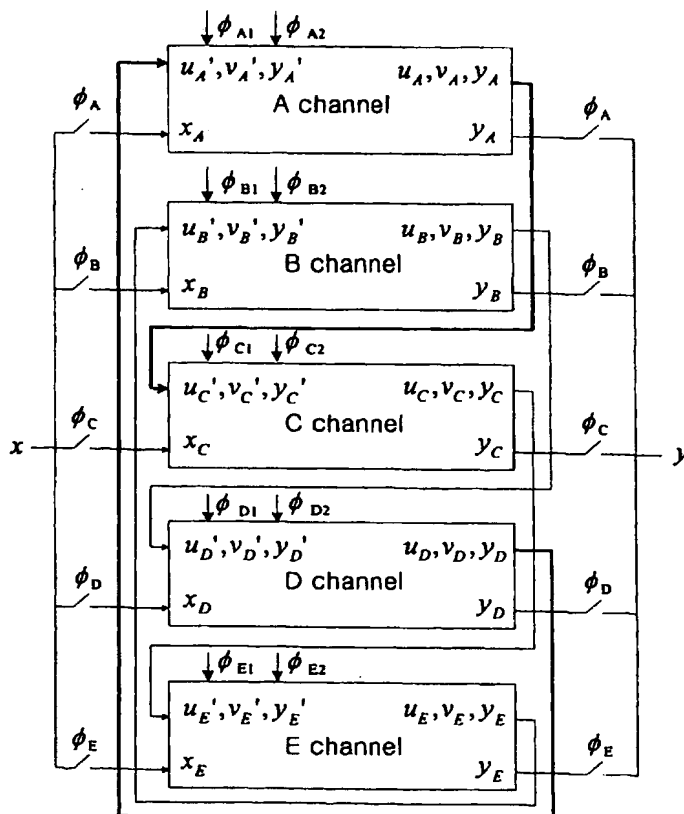
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(54) Title: TIME-INTERLEAVED BAND-PASS DELTA-SIGMA MODULATOR



(57) **Abstract:** Disclosed is a time-interleaved bandpass delta-sigma modulator, which includes a first adder and a second adder and a comparator. An input signal is inputted to the first adder according to an each channel block's clock frequency, and an n-th channel block's output U_n of the first adder is inputted to the first adder and the second adder of an (n+2)'th channel block, and an n-th block's output V_n of the second adder is inputted to the second adder of an (n+2)'th block, and an output Y_n that passes an n'th block's comparator is inputted to the first adder and the second adder of an (n+2)'th block. Therefore, a modulator of the present invention receives output of the each block's comparator sequentially and makes the final output Y.